

Spplication Serial No
Filing Date
nventor David R. Hembree
Assignee Micron Technology, Inc.
Group Art Unit
Examiner
Attorney's Docket No
Fitle: Methods of Processing Wafers and Methods of Communicating Signals With
Respect to a Wafer

RESPONSE TO MARCH 27, 2002 OFFICE ACTION

To:

Assistant Commissioner for Patents

From:

James D. Shaurette (Tel. 509-624-4276; Fax 509-838-3424)

Wells, St. John, P.S.

Washington, D.C. 20231

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3828

Sir:

Responsive to the Office Action dated March 27, 2002, Applicant amends and remarks as follows:

AMENDMENTS

In the Abstract

Please insert the following in place of the originally filed Abstract:

--An electronic device workpiece processing apparatus and method of communicating signals within an electronic device workpiece processing apparatus are provided. One embodiment of an electronic device workpiece processing apparatus includes a chuck including a surface, an electrical coupling adjacent the surface, and

electrical interconnect configured to connect with the electrical coupling of the chuck and

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